

AMENDMENTS TO THE CLAIMS:

1. (previously presented) An apparatus, comprising:

a plurality of received-signal registers which receive and store therein a plurality of respective received-signal sequences;

a selector which selects one of the received signal sequences stored in said received-signal registers;

at least one code register which stores therein a de-spreading-code sequence;

a multiplication circuit which multiplies the selected one of the received-signal sequences by the de-spreading-code sequence; and

a summation circuit which obtains a sum of results of the multiplication to obtain a correlation between the selected one of the received-signal sequences and the de-spreading-code sequence;

wherein said at least one code register includes a first code register storing a first de-spreading code and a second code register storing a second de-spreading code, wherein the pattern of the first de-spreading code and the pattern of the second de-spreading code are different, and said apparatus further comprising a selector which selects one of said first code register and said second code register to select and supply the de-spreading-code sequence to the multiplication circuit.

2. (canceled)

3. (original) The apparatus as claimed in claim 1, further comprising:

a delay-profile-holding unit which generates a delay profile based on correlations

BEST AVAILABLE COPY